



# Grade 1 and Grade 0 Safety Power System Basis Chip with CAN Flexible Data Transceiver

## FS6500

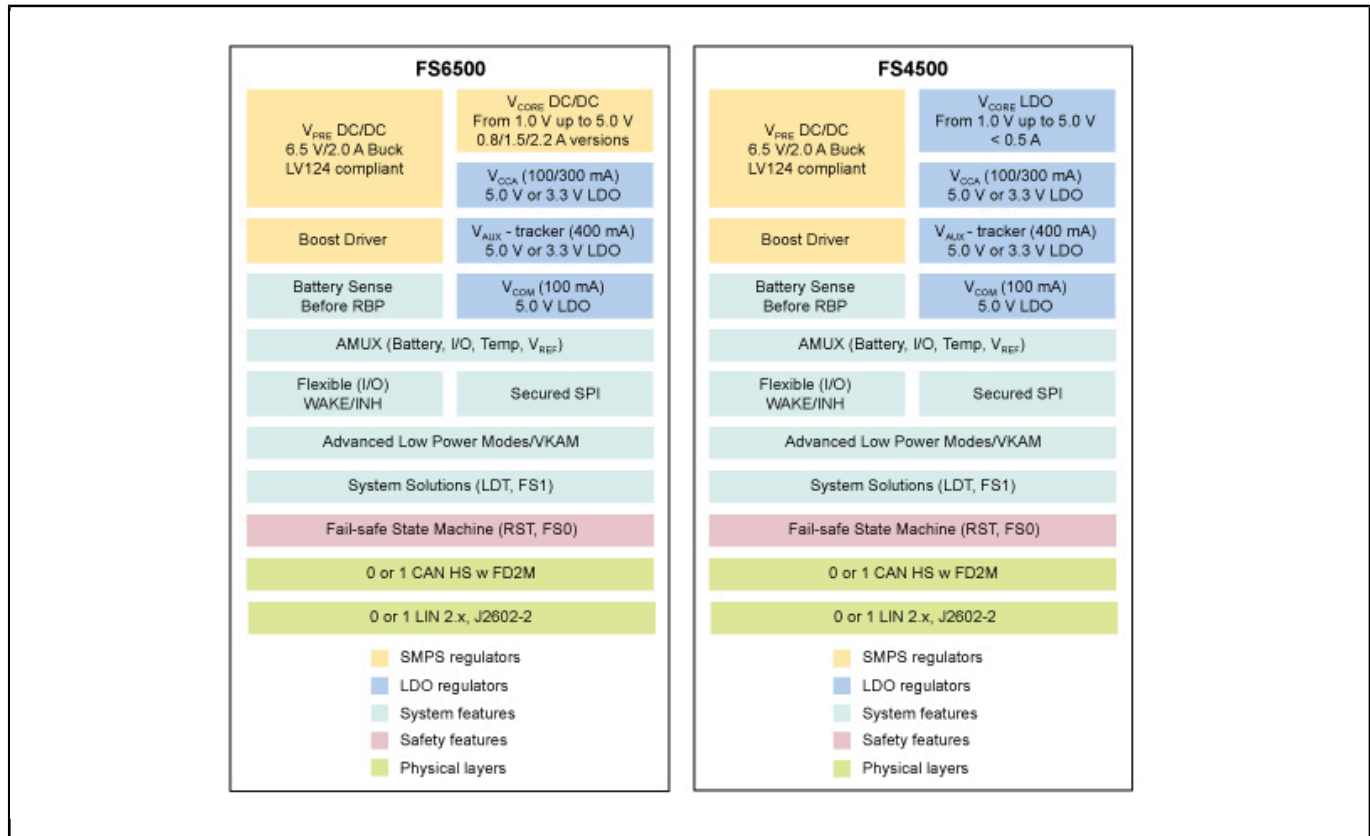
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The FS65 system basis chip (SBC) provides power to MCUs and optimizes energy consumption through DC-DC switching regulators, linear regulators and ultra-low-power saving modes.

Featuring:

- Advanced functional safety measures to target ASIL B/D applications
- A serial peripheral interface (SPI) to allow control and diagnostics with the MCU
- Integration of CAN FD and LIN physical interfaces compliant with the ISO 11898-2,-5, LIN 2.2, 2.1 /J2602-2 standards along with the latest automotive OEM standards for EMC and ESD
- A range of integrated safety features such as monitoring of critical analog parameters, a fail-safe state machine and an advanced watchdog reduce software complexity with dual-core lock-step MCUs
- High-temperature capability up to  $T_A = 150\text{ }^{\circ}\text{C}$  and  $T_J = 175\text{ }^{\circ}\text{C}$ , compliant with AEC-Q100 Grade 0 automotive qualification

## FS6500 Fail Silent SBC with DCDC, ASIL B/D ready Block Diagram



View additional information for [Grade 1 and Grade 0 Safety Power System Basis Chip with CAN Flexible Data Transceiver](#).

Note: The information on this document is subject to change without notice.

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