



# Ultra-Low-Power, Highly Integrated MCU

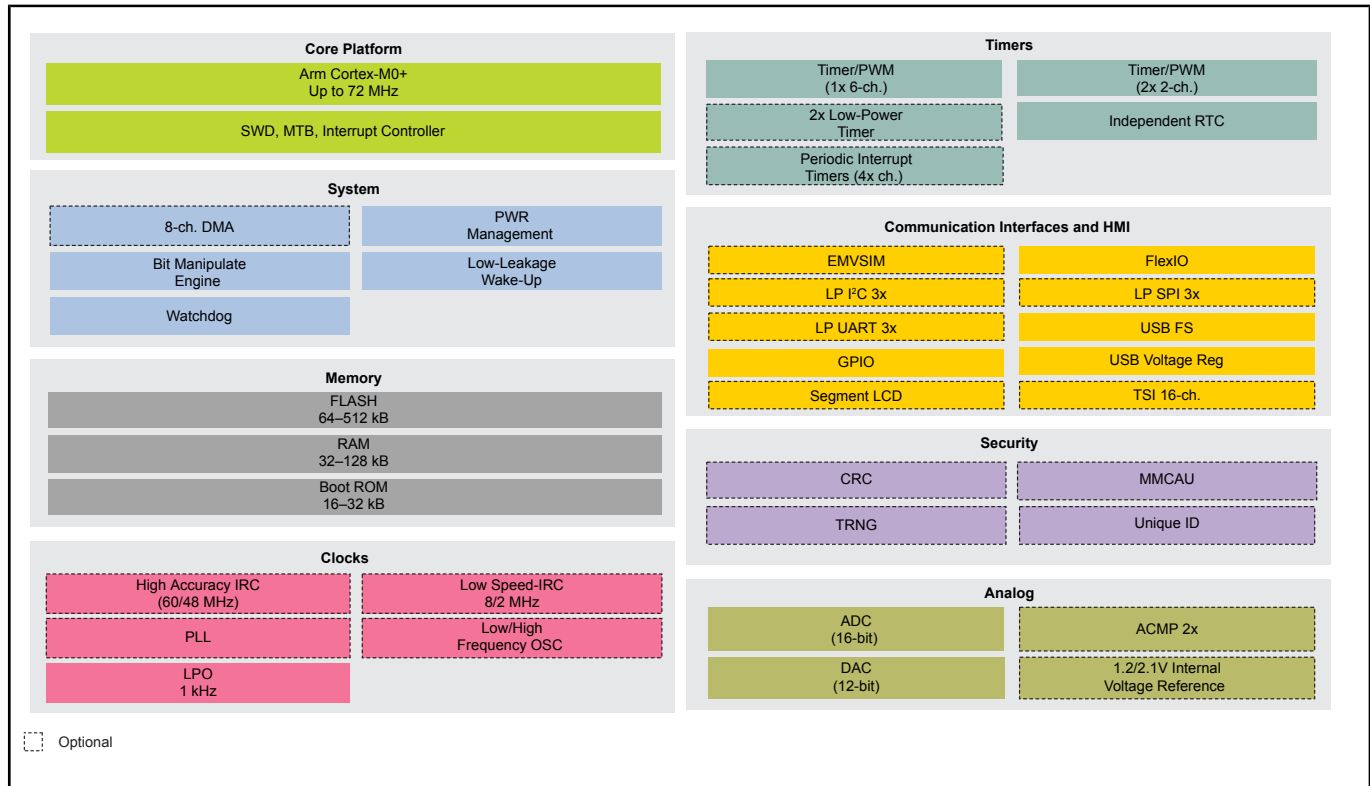
## K32-L2

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The K32 L2 MCU family's low-leakage architecture, combined with its power-optimized peripherals and security features (such as cryptographic acceleration technology, cyclic redundancy check and a true random number generator), make it ideal for consumer, industrial and IoT applications requiring a low-priced, power efficient option with longer battery life.

This family includes a low power Arm® Cortex®-M0+ core and with options scaling from 64 KB to 512 KB Flash and from 32 kB to 128 kB SRAM, the K32 L2 family offers a wide range of memory resources to fit different application tasks within a small-form factor, low power, and highly integrated design.

# K32 L2 MCU Block Diagram



View additional information for [Ultra-Low-Power, Highly Integrated MCU](#).

**Note:** The information on this document is subject to change without notice.

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