



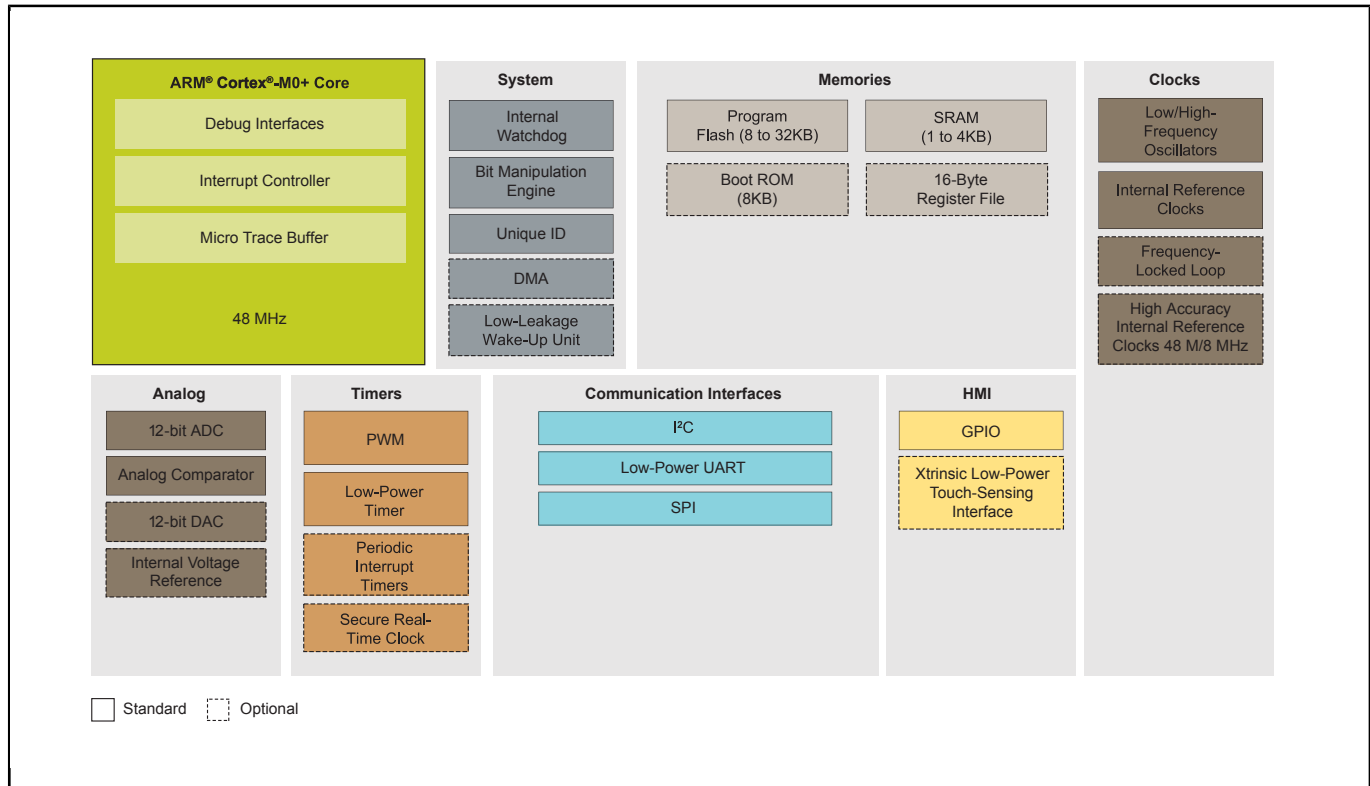
Kinetis® KL0x-48 MHz, Entry-Level Ultra-Low Power Microcontrollers (MCUs) based on Arm® Cortex®-M0+ Core

KL0x

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The Kinetis® KL0x MCU family is the entry point into the Kinetis L series based on the Arm® Cortex®-M0+ core. The KL0x ultra-low-power MCUs provide a bridge for 8-bit customers migrating into the Kinetis portfolio and is software and tool compatible with all other Kinetis L series devices.

Kinetis L Series KL0x MCUs Block Diagram



View additional information for [Kinetis® KL0x-48 MHz, Entry-Level Ultra-Low Power Microcontrollers \(MCUs\) based on Arm® Cortex®-M0+ Core](#).

Note: The information on this document is subject to change without notice.

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