512 KB Flash, 64 KB SRAM, Ethernet, No CAN LQFP100 Package

LPC1767FBD100

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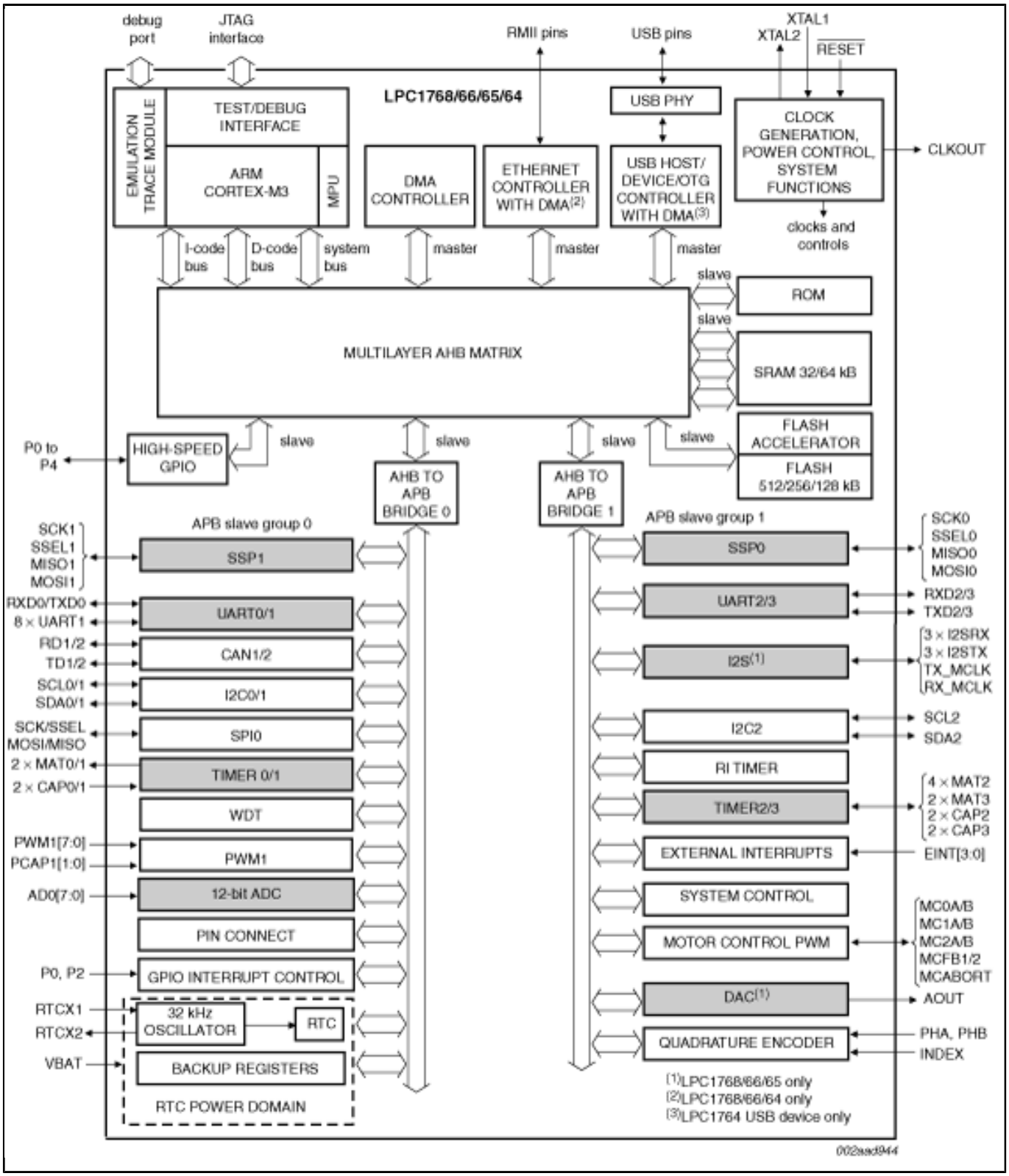
The LPC1767 is a Cortex®-M3 microcontroller for embedded applications featuring a high level of integration and low power consumption at frequencies of 100 MHz. Features include 512 kB of flash memory, 64 kB of data memory, Ethernet MAC, 8-channel DMA controller, 4 UARTs, 3 SSP/SPI, 3 I2C, I2S, 8-channel 12-bit ADC, 10-bit DAC, motor control PWM, Quadrature Encoder interface, 4 general purpose timers, 6-output general purpose PWM, ultra-low power Real-Time Clock with separate battery supply, and up to 70 general purpose I/O pins. The LPC1767 is pin-compatible to the 100-pin LPC2368 Arm7™ MCU

Block diagram: LPC1764FBD100, LPC1765FBD100, LPC1766FBD100, LPC1768FBD100

Block Diagram



View additional information for 512 KB Flash, 64 KB SRAM, Ethernet, No CAN LQFP100 Package.

Note: The information on this document is subject to change without notice.