



Low-cost, low-power Arm926EJ-S™ MCUs with high-speed USB 2.0 OTG, SD/MMC, and NAND flash controller

LPC3130FET180

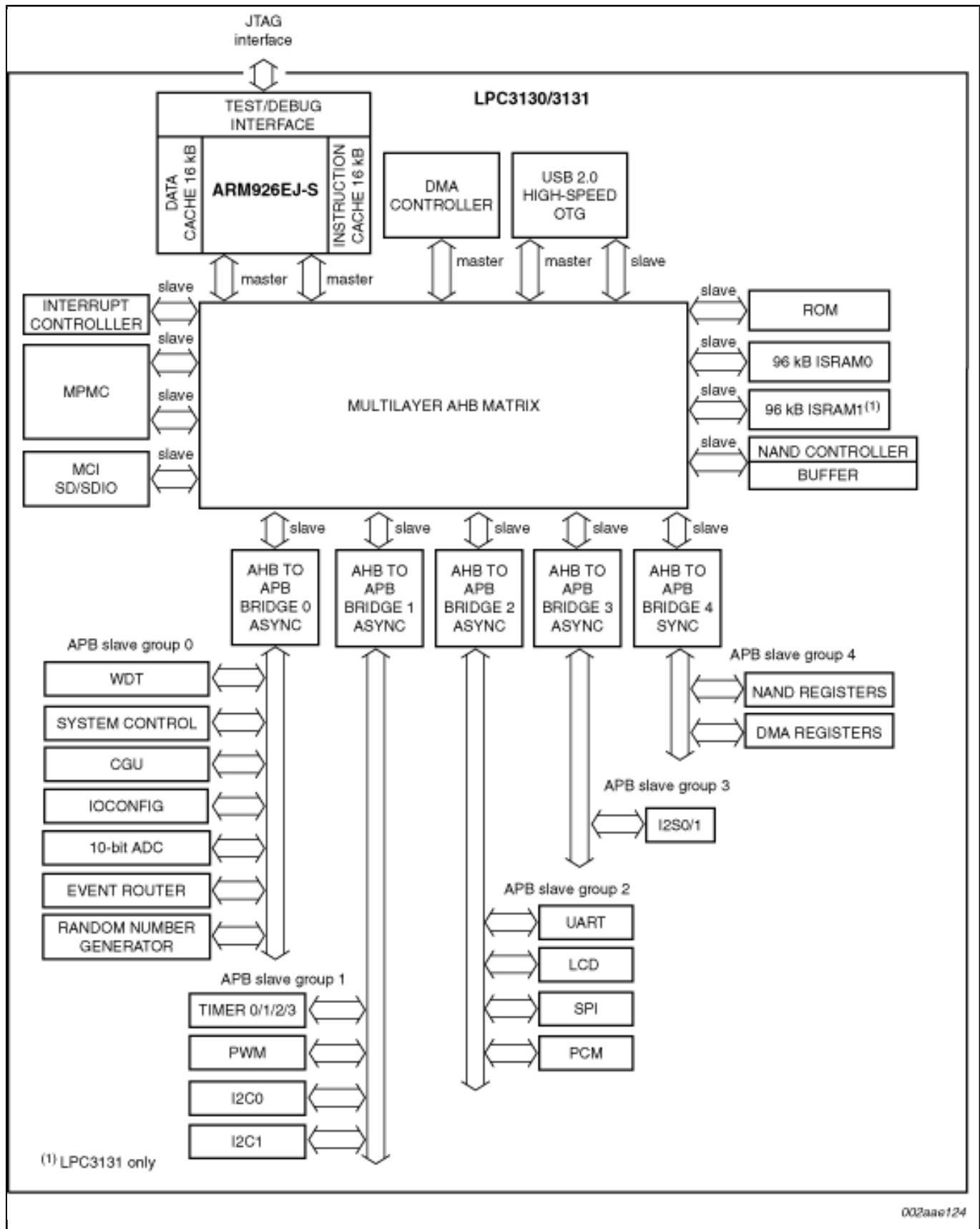
Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

Last Updated: Apr 8, 2022

The NXP® LPC3130/3131 combine an 180 MHz ARM926EJ-S CPU core, high-speed USB 2.0 On-The-Go (OTG), up to 192 KB SRAM, NAND flash controller, flexible external bus interface, four channel 10-bit ADC, and a myriad of serial and parallel interfaces in a single chip targeted at consumer, industrial, medical, and communication markets. To optimize system power consumption, the LPC3130/3131 have multiple power domains and a very flexible Clock Generation Unit (CGU) that provides dynamic clock gating and scaling.

Block diagram: LPC3130FET180, LPC3131FET180 Block Diagram



View additional information for [Low-cost, low-power Arm926EJ-S™ MCUs with high-speed USB 2.0 OTG, SD/MMC, and NAND flash controller.](#)

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.