



Multi-Channel (9) PMIC for S32G Processor – 8 High Power, 1 Low Power, Fit for ASIL D Safety Level

VR5510

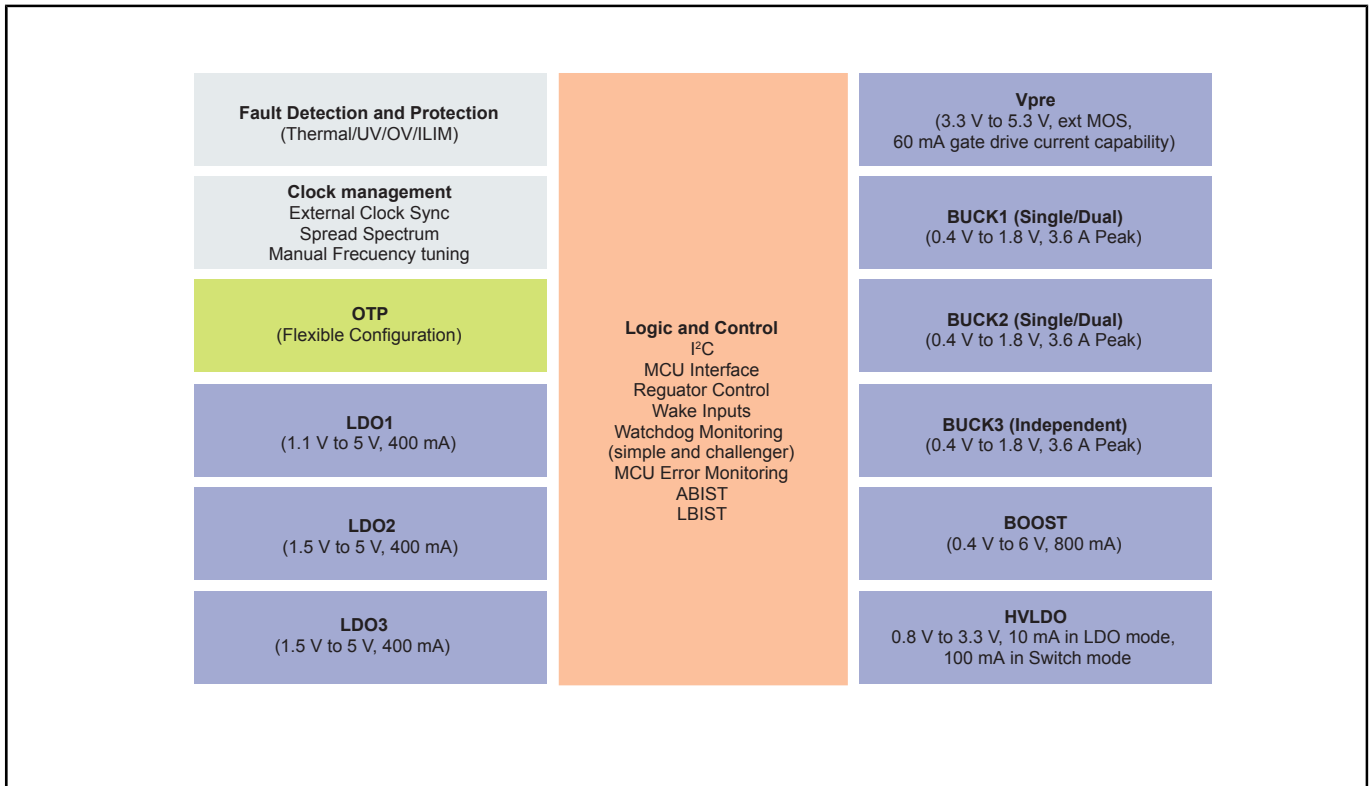
Last Updated: Mar 25, 2024

The VR5510 is NXP's multi-channel high-voltage PMIC device designed to be used for high performance automotive and industrial applications. The VR5510 is designed specifically for the [S32G vehicle network processor](#) family targeting service-oriented gateways, domain controllers, and safety controller applications.

Integrated and independent voltage monitoring circuits ensure compliance with ISO 26262 standard and functional safety up to ASIL D level. The VR5510 is also available as a standard ASIL B device or a non-safety device for applications that have different ISO compliance requirements.

The VR5510 is also suitable for a variety of applications including infotainment, ADAS, vision, and radar either as a standalone power solution or in combination with other NXP PMICs like PF502x family or PF8x family.

VR5510 Block Diagram



View additional information for [Multi-Channel \(9\) PMIC for S32G Processor – 8 High Power, 1 Low Power, Fit for ASIL D Safety Level](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.