



i.MX RT600 Crossover MCU with Arm[®] Cortex[®]-M33 and DSP Cores

i.MX-RT600

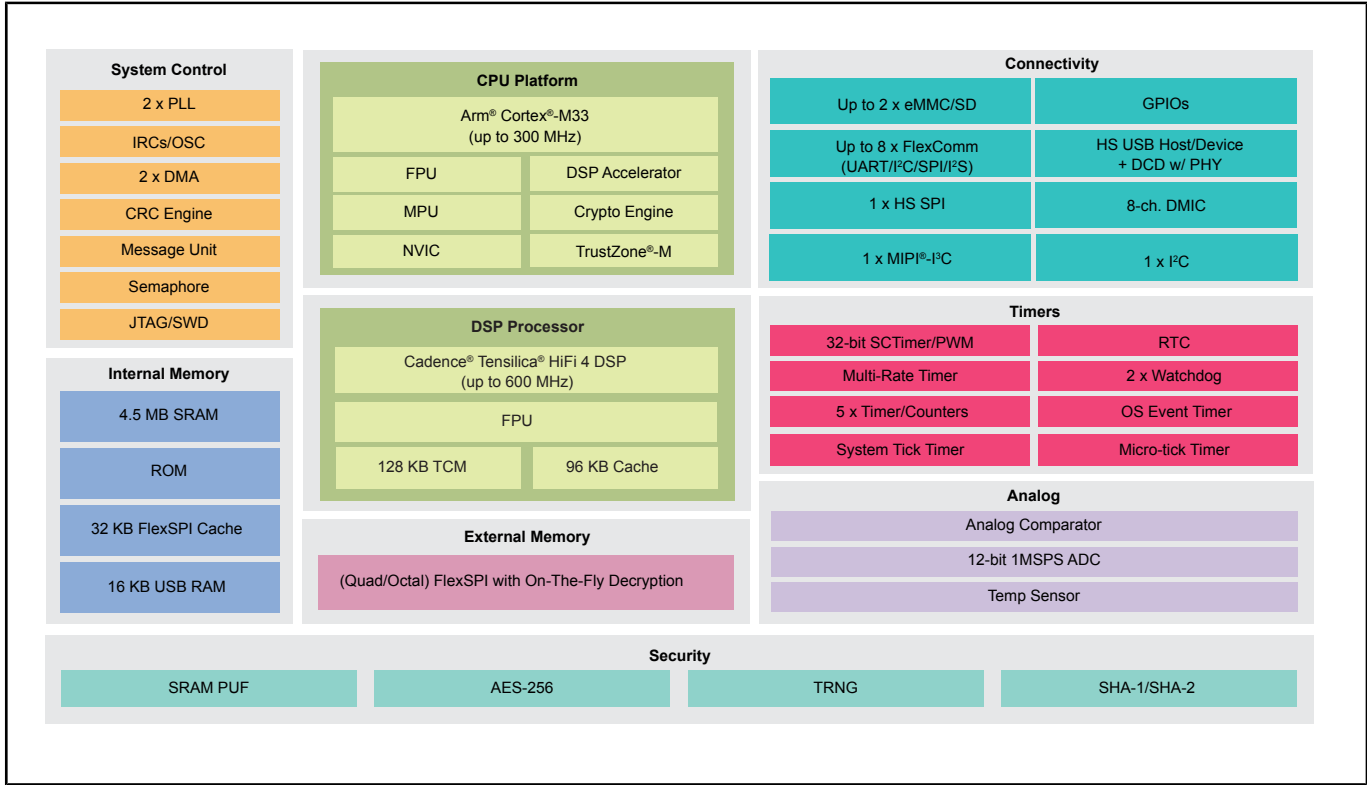
Last Updated: Apr 11, 2024

i.MX RT600 Crossover MCUs are dual-core devices featuring an Arm[®] Cortex[®]-M33 and Cadence[®] Xtensa[®] HiFi4 Audio DSP CPU designed for audio, voice and consumer IoT applications.

The i.MX RT600 CM33 operates at up to 300 MHz and includes two coprocessors providing enhanced performance. The HiFi4 can operate up to 600 MHz. The family offers a rich set of peripherals, embedded security and very low power consumption. The device has up to 4.5 MB SRAM and a FlexSPI with two ports and 32 KB cache.

The i.MX RT600 family is supported by the [MCUXpresso ecosystem](#), which includes an SDK, a choice of IDEs and secure provisioning and configuration tools to enable rapid development.

i.MX-RT600 Crossover MCU Block Diagram



View additional information for [i.MX RT600 Crossover MCU with Arm® Cortex®-M33 and DSP Cores.](#)

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.