Multimedia Applications Processors - LCD Controller, OpenVG™, Camera Interface, Arm11™ Core

i.MX357

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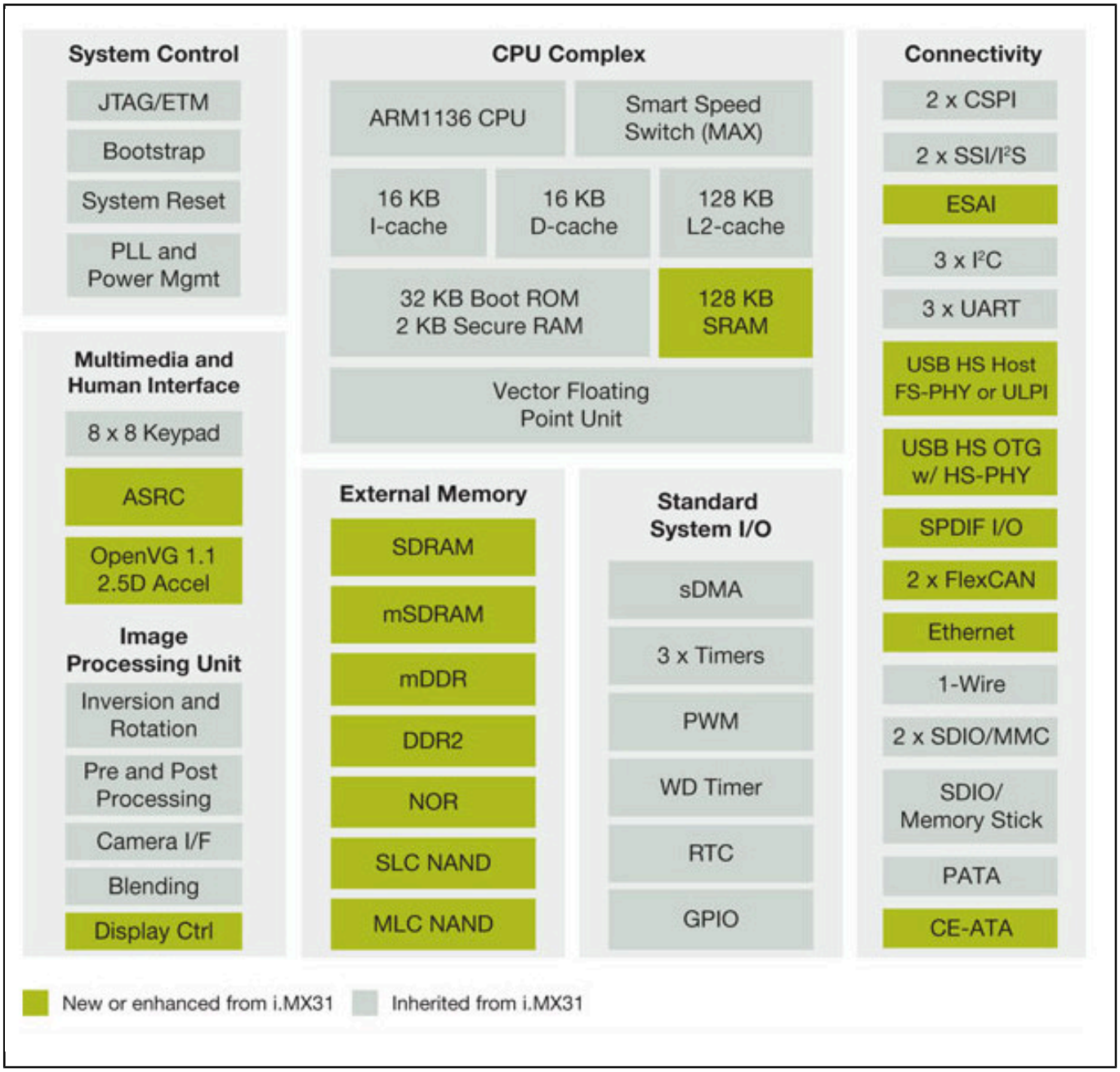
Our i.MX357 processor integrates an OpenVG™1.1 hardware accelerator to deliver smooth textural visuals. The integration of the LCD controller enables high resolution and high colour display. The Camera Interface (CMOS Sensor Interface) allows for video input acceptance, taking photos, streaming video, etc. To reduce BOM costs, it is packed with connectivity options including UARTs, SPIs, Ethernet, two Controller Area Network (CAN), two USB ports with integrated PHYs, three MMC/SD/SDIO ports, PATA, CE-ATA and the ability to connect to external wireless modules via a USB or SDIO port. Support for cost-effective memory options like DDR2 and multi-level cell NAND reduces system costs and provide the design engineer great flexibility.

The i.MX357 is supported by companion NXP® power management ICs (PMIC), MC13892 and MMPF0100.

i.MX357 Multimedia Applications Processor Block Diagram Block Diagram



View additional information for Multimedia Applications Processors - LCD Controller, OpenVG™, Camera Interface, Arm11™ Core.

Note: The information on this document is subject to change without notice.