



# i.MX 6SLL Processors – Single-Core Processor with Arm® Cortex®-A9 Core

## i.MX6SLL

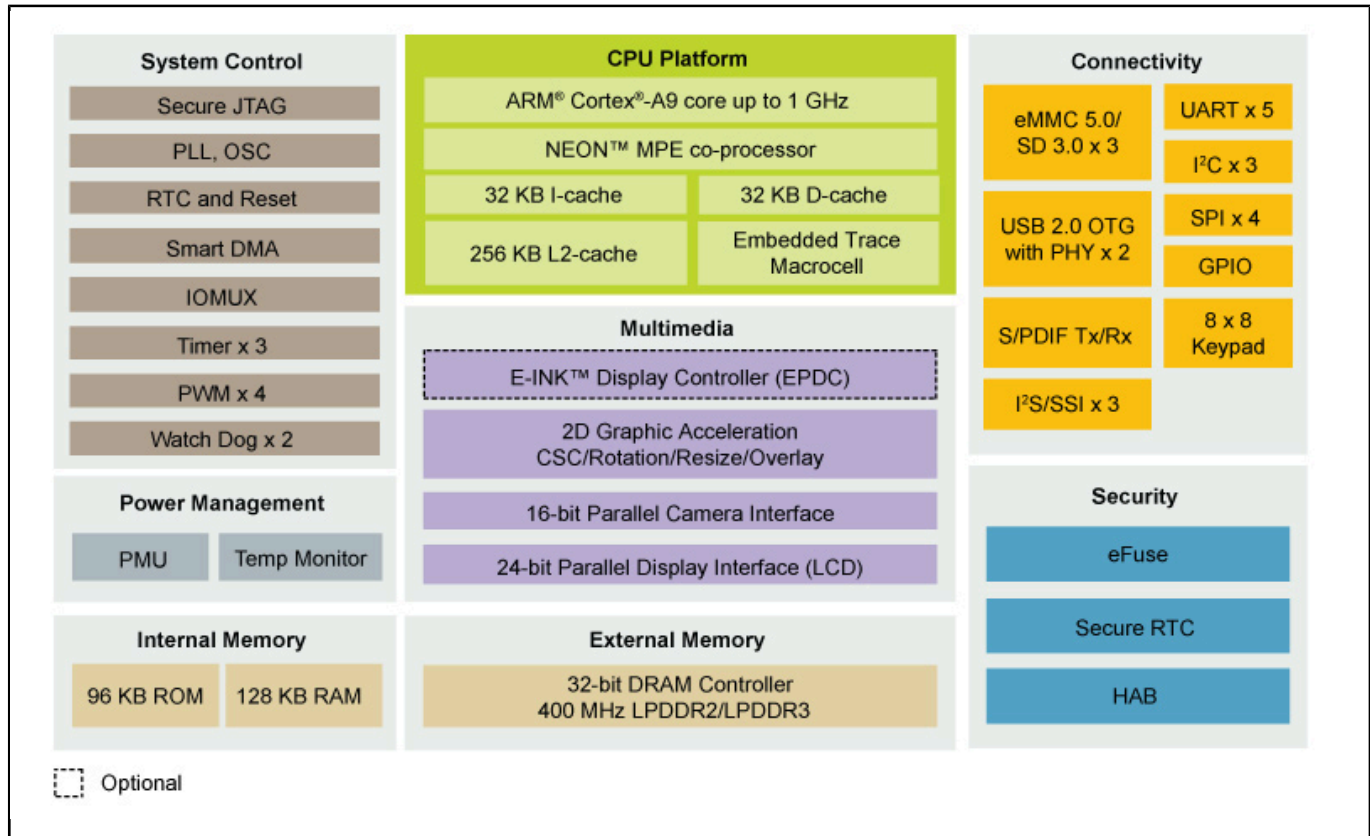
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The i.MX 6SLL processor represents NXP's latest achievement in integrated multimedia applications processors, which are part of a growing family of multimedia-focused products that offer high-performance processing and are optimized for lowest power consumption.

The processor features NXP's advanced implementation of a single Arm® Cortex®-A9, which operates at speeds up to 1GHz. The processor provides a 32-bit DDR interface that supports LPDDR2 and LPDDR3. In addition, there are a number of other interfaces for connecting peripherals, such as WLAN, Bluetooth™, GPS, hard drive, displays and camera sensors.

i.MX 6 applications processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

## i.MX 6SLL Multimedia Applications Processor Block Diagram Block Diagram



View additional information for [i.MX 6SLL Processors - Single-Core Processor with Arm® Cortex®-A9 Core](#).

Note: The information on this document is subject to change without notice.

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